## BEST AVAILABLE CO. U.S. UTILITY Patent Application

PATENT NUMBER and ISSUE DATE

**EXAMINER** FILING DATE CLASS SUBCLASS GAU APPL NUM 632 10076450 02/19/2002 257 2815 Meister Ramdani Jamal; Droopad Ravindranath; Hilt Lyndee; Eisenbeiser Kurt; \*\*APPLICANTS: \*\*CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/502,023 02/10/2000 \*\* FOREIGN APPLICATIONS VERIFIED: RESCIND -PG-PUB DO NOT PUBLISH 🖵 ATTORNEY DOCKET NO u yes u no Foreign priority claimed 35 USC 119 conditions met 🗆 yes 🗅 no 219625US99DIV Verified and Acknowledged Examiners's intials TITLE: Semiconductor structure, semiconductor device, communicating device, integrated circuit, and process for fabricating the same U.S.DEPT, OF COMM./PAT.& TM-PTO-436L(Rev. 12-94 CLAIMS ALLOWED

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		Assistant Examiner	Total Claims	Pri: O.G	nt Claim for
			DRAWING		
Amount Due		1	Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL DISCLAMER		Primary Examiner	A self-offen Framines		
		PREPARED FOR ISSUE			
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